



# NASCUG VII

**NASCUG 2007**  
**Jack Donovan, Chairman**  
**Co-Founder ESLX, Inc.**  
**Room 33 A-C, San Diego Convention Center**  
**June 4, 2007**

# North American SystemC User's Group

- **Goal: Accelerate the use of SystemC and Electronic System Level design by promoting the sharing of ideas among users and potential users**
  - [www.nascug.org](http://www.nascug.org)
  - Presentations picked and reviewed by a panel of users
  - Meeting Organized by Users
    - ◆ Tor Jeremiassen - Texas Instruments
    - ◆ Jean-François Boland - École de technologie supérieure
- **Closely aligned with OSCI**
  - [www.systemc.org](http://www.systemc.org)
  - Provide financial and logistical support

# Thanks To Our Reviewers

- Ahmed Jerraya
- Ambar Sarkar - Paradigm Works
- Bill Murray
- Bill Dittenhoffer - HP Inc.
- Brian Bailey - Brian Bailey Consulting
- Lawrence Case - Freescale
- Joe Chapman - The MITRE Corp.
- David Lou - Analog Devices, Inc.
- Grant Martin - Tensilica
- Greg Tumbush - AMI Semiconductor
- Guy Bois - École Polytechnique de Montréal
- Kevin Heuer - Freescale
- Joan Bartlett - Actis Design
- Michel Langevin - ST Micro
- Rene Lemoyne -
- Sandro Rigo - UNICAMP
- Stuart Swan - Cadence

# AGENDA

- 2:15 – 2:25 **Welcome & Agenda**
- 2:25 – 2:45 **Verification of a SpaceWire C&DH Bus Using SystemC**  
*James Petrus, Lockheed Martin Space Systems Company*
- 2:45 – 3:10 **Understanding Specialized Ports and TLM Interconnects**  
*David Black, ESLX, Inc.*
- 3:10 – 3:30 **DDRx Based Memory System Behavioral Model for Architectural Exploration and Performance Analysis**  
*Leena Singh, Rambus Inc.*
- 3:30 – 3:45 **Break**
- 3:45 – 4:00 **OSCI TLM Working Group Update**  
*Trevor Wieman, Intel Corp.*
- 4:00 – 4:10 **What's New from GreenSocs**  
*Mark Burton, GreenSocs*
- 4:10 – 4:30 **The Application of SystemC to the Design and Implementation of a High Rate Satellite Transceiver**  
*Joseph Chapman, The MITRE Corp.*
- 4:30 – 5:00 **Software Performance Measurement in High Speed Virtual Platforms**  
*Puneet Maheshware, CoWare Inc.*
- 5:00 – 6:00 **Survey, Door Prize, Networking Reception**

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# Wrap

- **Please Completely Fill Out the Survey**
  - Your Survey is an entry for a door prize
    - ◆ Signed “ESL Design and Verification” - booth 3078, Elsevier
    - ◆ Signed “SystemC:From the Ground Up” - booth 3664, Springer
  - Surveys are used to improve the next meeting
- **Join Us for **NASCUG VIII****
  - Stay Tuned for Announcements
  - Consider Doing a Presentation
  - Interested in Reviewing Presentations? Email me!
  - Thank You to our Technical Co-Chairs
    - ◆ Tor Jeremiassen - Texas Instruments
    - ◆ Jean-François Boland - École de technologie supérieure

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