

# Accellera Systems Initiative Update

Shishpal Rawat, Chairman | February 27, 2012



# News: Accellera and OSCI Merge



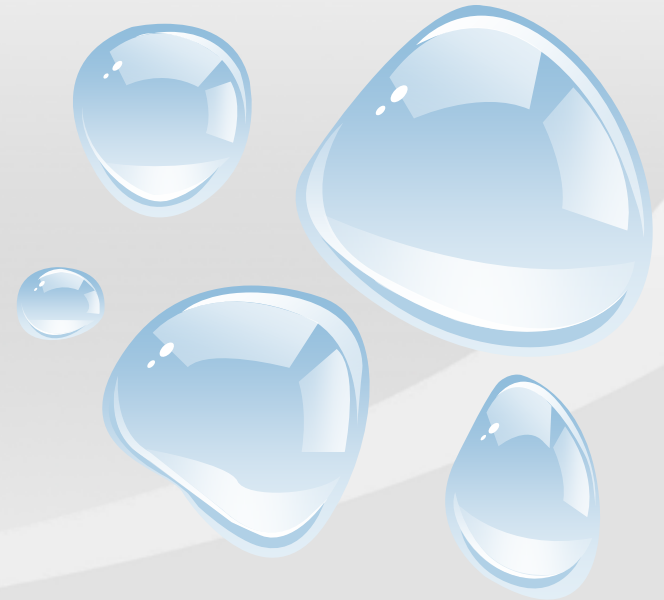
**“Our new organization will leverage the excellent work of our technical committees to provide a bigger benefit to the electronics industry. By forming a combined organization, we will be able to accelerate development of system level standards that will move electronic design productivity to the next level.”**

**Shishpal Rawat, Accellera Systems Initiative Chair**

# Accellera Systems Initiative

## Our Mission

- To provide design and verification standards required by systems, semiconductor, IP and design tool companies to enhance a front-end design automation process.
- To collaborate with its community of companies, individuals and organizations in delivering the standards that lower the cost to design commercial EDA, IC and embedded system solutions.




# Merger Update

- **Integration of Accellera and OSCI Organizations - WIP**
  - Ongoing review of operational procedures and policies
  - Leveraging strengths from both organizations to expand applicability and increase adoption
  - Focused on next generation of EDA and IP standards
- **Continue to providing free downloads of IEEE standards**
  - IEEE 1666 SystemC available since mid-January
  - IEEE 1685 IP-XACT downloads going strong
- **New Technical Excellence Award Program Launched**
  - Candidates peer-nominated from across the organization
  - Recognizes work by people across multiple Accellera Systems Initiative standards

# Accellera Systems Initiative - Integration

- **Organizational Structure in Review**
  - Procedures and Policies
  - IP Rights
  - Web Infrastructure, User forums
- **Expanding DVCon as “the” premier conference addressing functional design and verification**
  - *Accellera Systems Initiative Day* Monday Feb. 27<sup>th</sup>
  - Full conference registration & Monday tutorial attendance up from last year
- **Expanding Global Sponsorship Program**
  - Opportunity for multiple companies to co-sponsor our events
  - Videos of all DVCon Monday tutorials and NASCUG meeting will be available on [www.accelera.org](http://www.accelera.org)

# IEEE 1666 SystemC Downloads

IEEE STANDARDS ASSOCIATION 

**IEEE Standard for Standard SystemC® Language Reference Manual**

IEEE Computer Society

Sponsored by the  
Design Automation Standards Committee

---

IEEE  
3 Park Avenue  
New York, NY 10016-5997  
USA

**IEEE Std 1666™-2011**  
(Revision of  
IEEE Std 1666-2005)

9 January 2012

- 47,165 downloads of IEEE1666-2005 through January 2012
- 3,756 downloads of IEEE1666-2011 from January 11 through January 31

<http://standards.ieee.org/getieee/1666/download/1666-2011.pdf>

# IEEE 1685 IP-XACT Downloads



**IEEE Standard for IP-XACT,  
Standard Structure for Packaging,  
Integrating, and Reusing IP within  
Tool Flows**

IEEE Computer Society  
and the  
IEEE Standards Association Corporate Advisory Group

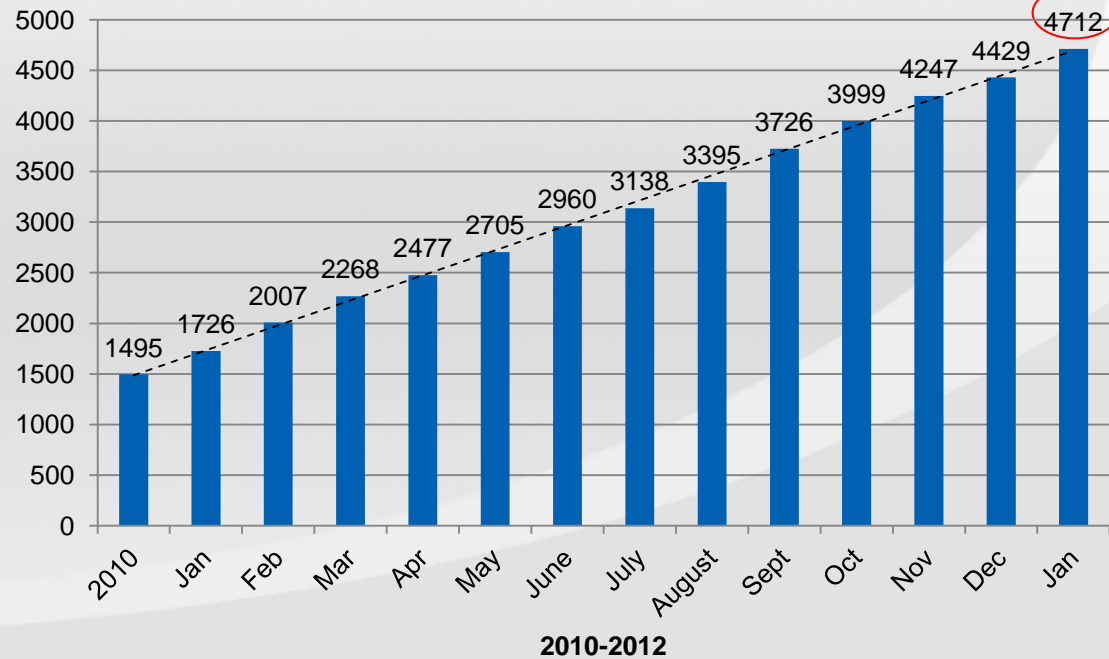
Sponsored by the  
Design Automation Standards Committee

1685™

IEEE  
3 Park Avenue  
New York, NY 10016-5997, USA  
18 February 2010

IEEE Std 1685™-2009

## Cumulative Downloads



<http://standards.ieee.org/getieee/1685/download/1685-2009.pdf>

# 2012 Technical Excellence Award



## John Aynsley

- For his dedication to the SystemC language and community.
- His contributions span the full range of SystemC language, transaction-level modeling and configuration.
- Instrumental in the various working groups converging on technically sound solutions.



# SystemC users Scope Expands

- **Unified Coverage Interoperability Standard**
- **Universal Verification Methodology (UVM) 2.0**
- **Verilog and SystemC Analog/Mixed-Signal (AMS)**
- **IP-XACT and System RDL**

# Summary

- **Organizational Structure Refinement in Progress; Expanded Scope for all in combined organization**
- **Establishing DVCon as “goto” conference for functional design and verification**
- **Expanding Global Sponsorship Program**